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Keltcher, C.N.; McGrath, K.J.; Ahmed, A.; Conway, P.;
Micro, IEEE
Volume 23, Issue 2, March-April 2003 Page(s):66 - 76
Digital Object Identifier 10.1109/MM.2003.1196116

AbstractPlus | References | Full Text: PDF(286 KB)   IEEE JNL
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☐ 3. **The Mips R4000 processor**
Mirapuri, S.; Woodacre, M.; Vasseghi, N.;
Micro, IEEE
Volume 12, Issue 2, April 1992 Page(s):10 - 22
Digital Object Identifier 10.1109/40.127580

AbstractPlus | Full Text: PDF(1120 KB)   IEEE JNL
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☐ 4. **Architectural overview of HaL systems**
Wilcke, W.W.;
Compcon '95.'Technologies for the Information Superhighway'. Digest of Papers.
5-9 March 1995 Page(s):251 - 258
Digital Object Identifier 10.1109/CMPCON.1995.512393

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   Smyth, N.; McLoone, M.; McCanny, J.V.;
   Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuits and Systems I: Fu
   and Applications, IEEE Transactions on]
   Volume 53, Issue 7, July 2006 Page(s):1506 - 1520
   Digital Object Identifier 10.1109/TCSI.2006.877888

   AbstractPlus | Full Text: PDF(768 KB)   IEEE JNL
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2. **A digital signal processor with IEEE floating-point arithmetic**
   Sohie, G.R.L.; Kloker, K.L.;
   Micro, IEEE
   Volume 8, Issue 6, Dec. 1988 Page(s):49 - 67
   Digital Object Identifier 10.1109/40.16780

   AbstractPlus | Full Text: PDF(1444 KB)   IEEE JNL
   Rights and Permissions

3. **A proposed scalable parallel open architecture data acquisition system for low to high rate ‹
   beams and all SSC detectors**
   Barsotti, E.; Booth, A.; Bowden, M.; Swoboda, C.; Lockyer, N.; VanBerg, R.;
   Nuclear Science, IEEE Transactions on
   Volume 37, Issue 3, Part 1-2, June 1990 Page(s):1216 - 1221
   Digital Object Identifier 10.1109/23.57369

   AbstractPlus | Full Text: PDF(576 KB)   IEEE JNL
   Rights and Permissions

4. **Future directions in microprocessor technology**
   Marks, M.P.;
   Solid-State Circuits, IEEE Journal of
   Volume 30, Issue 4, April 1995 Page(s):371 - 374
   Digital Object Identifier 10.1109/4.375955

   AbstractPlus | Full Text: PDF(348 KB)   IEEE JNL
   Rights and Permissions

5. **The implementation of P^{3}I, a parallel architecture for video real-time processing: a case stuc**
   Colaitis, M.-J.; Jumpertz, J.-L.; Guerin, B.; Cheron, B.; Battini, F.; De Lescure, B.; Gautier, E.; Geffr
   Proceedings of the IEEE
   Volume 84, Issue 7, July 1996 Page(s):1019 - 1037

Digital Object Identifier 10.1109/5.503301

AbstractPlus | References | Full Text: PDF(2144 KB)   IEEE JNL
Rights and Permissions

6. **MMX technology extension to the Intel architecture**
Peleg, A.; Weiser, U.;
Micro, IEEE
Volume 16, Issue 4, Aug. 1996 Page(s):42 - 50
Digital Object Identifier 10.1109/40.526924

AbstractPlus | References | Full Text: PDF(1228 KB)   IEEE JNL
Rights and Permissions

7. **Direct drive options for electric propulsion systems**
Hamley, J.A.;
Aerospace and Electronic Systems Magazine, IEEE
Volume 11, Issue 2, Feb. 1996 Page(s):20 - 24
Digital Object Identifier 10.1109/62.484301

AbstractPlus | Full Text: PDF(428 KB)   IEEE JNL
Rights and Permissions

8. **Itanium processor microarchitecture**
Sharangpani, H.; Arora, H.;
Micro, IEEE
Volume 20, Issue 5, Sept.-Oct. 2000 Page(s):24 - 43
Digital Object Identifier 10.1109/40.877948

AbstractPlus | References | Full Text: PDF(544 KB)   IEEE JNL
Rights and Permissions

9. **PipeRench: a reconfigurable architecture and compiler**
Goldstein, S.C.; Schmit, H.; Budiu, M.; Cadambi, S.; Moe, M.; Taylor, R.R.;
Computer
Volume 33, Issue 4, April 2000 Page(s):70 - 77
Digital Object Identifier 10.1109/2.839324

AbstractPlus | References | Full Text: PDF(368 KB)   IEEE JNL
Rights and Permissions

10. **A 600-MHz VLIW DSP**
Agarwala, S.; Anderson, T.; Hill, A.; Ales, M.D.; Damodaran, R.; Wiley, P.; Mullinnix, S.; Leach, J.; I
Rajagopal, A.; Chachad, A.; Agarwala, M.; Apostol, J.; Krishnan, M.; Duc Bui; Quang An; Nagaraj,
Elappuparackal, T.T.;
Solid-State Circuits, IEEE Journal of
Volume 37, Issue 11, Nov. 2002 Page(s):1532 - 1544
Digital Object Identifier 10.1109/JSSC.2002.803954

AbstractPlus | References | Full Text: PDF(1154 KB)   IEEE JNL
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11. **The AMD Opteron processor for multiprocessor servers**
Keltcher, C.N.; McGrath, K.J.; Ahmed, A.; Conway, P.;
Micro, IEEE
Volume 23, Issue 2, March-April 2003 Page(s):66 - 76
Digital Object Identifier 10.1109/MM.2003.1196116

AbstractPlus | References | Full Text: PDF(286 KB)   IEEE JNL
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12. **Itanium 2 processor microarchitecture**
McNairy, C.; Soltis, D.;
Micro, IEEE
Volume 23, Issue 2, March-April 2003 Page(s):44 - 55
Digital Object Identifier 10.1109/MM.2003.1196114

AbstractPlus | References | Full Text: PDF(303 KB)   IEEE JNL
Rights and Permissions

13. **Retargeting sequential image-processing programs for data parallel execution**
Baumstark, L.B., Jr.; Wills, L.M.;
Software Engineering, IEEE Transactions on
Volume 31, Issue 2, Feb. 2005 Page(s):116 - 136
Digital Object Identifier 10.1109/TSE.2005.26

AbstractPlus | Full Text: PDF(2800 KB)   IEEE JNL
Rights and Permissions

14. **A development and implementation of DSP based DSTATCOM to compensate unbalanced n**
Mishra, M.K.; Karthikeyan, K.; Linash, P.K.;
Power India Conference, 2006 IEEE
10-12 April 2006 Page(s):8 pp.
Digital Object Identifier 10.1109/POWERI.2006.1632501

AbstractPlus | Full Text: PDF(364 KB)   IEEE CNF
Rights and Permissions

15. **3D audio-video processing on the terrestrial DMB**
Sukhee Cho; Kugjin Yun; Namho Hur; Soo-In Lee;
Consumer Electronics, 2006. ICCE '06. 2006 Digest of Technical Papers. International Conference
7-11 Jan. 2006 Page(s):275 - 276
Digital Object Identifier 10.1109/ICCE.2006.1598417

AbstractPlus | Full Text: PDF(2328 KB)   IEEE CNF
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16. **Modeling the network processor and package for power delivery analysis**
Wei Cui; Parmar, P.; Morgan, J.M.; Sheth, U.;
Electromagnetic Compatibility, 2005. EMC 2005. 2005 International Symposium on
Volume 3, 8-12 Aug. 2005 Page(s):690 - 694 Vol. 3
Digital Object Identifier 10.1109/ISEMC.2005.1513612

AbstractPlus | Full Text: PDF(616 KB)   IEEE CNF
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17. **Extended split-issue: enabling flexibility in the hardware implementation of NUAL VLIW DSP**
Iyer, B.; Srinivasan, S.; Jacob, B.;
Computer Architecture, 2004. Proceedings. 31st Annual International Symposium on
19-23 June 2004 Page(s):364 - 375
Digital Object Identifier 10.1109/ISCA.2004.1310788

AbstractPlus | Full Text: PDF(424 KB)   IEEE CNF
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18. **Parallel processing for computationally intensive speech analysis operations**
Rice, T.; Siegel, L.;
Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '83,
Volume 8, Apr 1983 Page(s):471 - 474

AbstractPlus | Full Text: PDF(152 KB)   IEEE CNF
Rights and Permissions

19. **Single event effects hardening and characterization of Honeywell's RHPPC integrated circui**
Lintz, J.P.; Hoffmann, L.F.; Bastyr, D.J.; Brown, G.R.; Nelson, D.K.;
Radiation Effects Data Workshop, 2003. IEEE
21-25 July 2003 Page(s):156 - 164
Digital Object Identifier 10.1109/REDW.2003.1281369

AbstractPlus | Full Text: PDF(414 KB)   IEEE CNF
Rights and Permissions

20. **Preliminary evaluation of a binary translation system for multithreaded processors**
Ootsu, K.; Yokota, T.; Ono, T.; Baba, T.;
Innovative Architecture for Future Generation High-Performance Processors and Systems, 2002, Ir
on
10-11 Jan. 2002 Page(s):77 - 84
Digital Object Identifier 10.1109/IWIA.2002.1035021

AbstractPlus | Full Text: PDF(1363 KB)   IEEE CNF
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21. **An eight-issue tree-VLIW processor for dynamic binary translation**
Ebcioglu, K.; Fritts, J.; Kosonocky, S.; Gschwind, M.; Altman, E.; Kailas, K.; Bright, T.;
Computer Design: VLSI in Computers and Processors, 1998. ICCD '98. Proceedings., Internationa
5-7 Oct. 1998 Page(s):488 - 495
Digital Object Identifier 10.1109/ICCD.1998.727094

AbstractPlus | Full Text: PDF(108 KB)   IEEE CNF
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22. **A VLSI implementation of image processor for facsimile and digital copier**
Chang Dae Park; Young Hoon Jeong; Hyoung Soo Kim; Ki Sang Hong; Ki Wong Park; Yoon Soo K
Circuits and Systems, 1997. Proceedings of the 40th Midwest Symposium on
Volume 2, 3-6 Aug. 1997 Page(s):1457 - 1460 vol.2
Digital Object Identifier 10.1109/MWSCAS.1997.662359

AbstractPlus | Full Text: PDF(468 KB)   IEEE CNF
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23. **Intel's MMX^{TM} technology-a new instruction set extension**
Lempel, O.; Peleg, A.; Weiser, U.;
Compcon '97. Proceedings, IEEE
23-26 Feb. 1997 Page(s):255 - 259
Digital Object Identifier 10.1109/CMPCON.1997.584723

AbstractPlus | Full Text: PDF(400 KB)   IEEE CNF
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24. **An implementation of a high-level language for a data-driven processor**
Yamasaki, T.; Munakata, K.; Shima, K.; Yoshida, S.; Terada, H.;
Signals, Systems and Computers, 1991. 1991 Conference Record of the Twenty-Fifth Asilomar Co
4-6 Nov. 1991 Page(s):79 - 84 vol.1
Digital Object Identifier 10.1109/ACSSC.1991.186418

AbstractPlus | Full Text: PDF(316 KB)   IEEE CNF
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25. **Parallel multi-context architecture with high-speed synchronization mechanism**
Toda, K.; Nishida, K.; Uchibori, Y.; Sakai, S.; Shimada, T.;
Parallel Processing Symposium, 1991. Proceedings., Fifth International
30 April-2 May 1991 Page(s):336 - 343
Digital Object Identifier 10.1109/IPPS.1991.153800

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☐ 26. **The architecture of the LR33000: a MIPS compatible RISC processor for embedded control a**
Caulk, B.; Desai, S.; Gavrielov, M.; Harper, G.; Jones, D.; Kwong, M.; Murzello, M.; Oke, T.; Patel, Yang, R.;
Computer Design: VLSI in Computers and Processors, 1991. ICCD '91, Proceedings., 1991 IEEE I
Conference on
14-16 Oct. 1991 Page(s):333 - 336
Digital Object Identifier 10.1109/ICCD.1991.139911

AbstractPlus | Full Text: PDF(276 KB)   IEEE CNF
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☐ 27. **Dynamically scheduled VLIW processors**
Rau, B.R.;
Microarchitecture, 1993. Proceedings of the 26th Annual International Symposium on
1-3 Dec. 1993 Page(s):80 - 92
Digital Object Identifier 10.1109/MICRO.1993.282754

AbstractPlus | Full Text: PDF(1104 KB)   IEEE CNF
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☐ 28. **Evaluation of pseudo vector processor based on slide-windowed registers**
Nakamura, H.; Imori, H.; Yamashita, Y.; Nakazawa, K.; Boku, T.; Li, H.; Nakata, I.;
System Sciences, 1994. Vol. I: Architecture. Proceedings of the Twenty-Seventh Hawaii Internatior
Volume 1, 4-7 Jan. 1994 Page(s):368 - 377
Digital Object Identifier 10.1109/HICSS.1994.323156

AbstractPlus | Full Text: PDF(896 KB)   IEEE CNF
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☐ 29. **Pseudo vector processor for high-speed list vector computation with hiding memory access**
Nakamura, H.; Wakabayashi, T.; Nakazawa, K.; Boku, T.; Wada, H.; Inagami, Y.;
TENCON '94. IEEE Region 10's Ninth Annual International Conference. Theme: 'Frontiers of Comr
Proceedings of 1994
22-26 Aug. 1994 Page(s):338 - 342 vol.1
Digital Object Identifier 10.1109/TENCON.1994.369282

AbstractPlus | Full Text: PDF(400 KB)   IEEE CNF
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☐ 30. **The Mips R4000 processor**
Mirapuri, S.; Woodacre, M.; Vasseghi, N.;
Micro, IEEE

Volume 12, Issue 2, April 1992 Page(s):10 - 22
Digital Object Identifier 10.1109/40.127580

AbstractPlus | Full Text: PDF(1120 KB)   IEEE JNL
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31. **The ST18940/41: an advanced single-chip digital signal processor**
Picco, A.; Michalina, J.C.; Laurier, B.; Fuin, D.; Menut, P.; Laborie, J.L.;
Circuits and Systems, 1989., IEEE International Symposium on
8-11 May 1989 Page(s):1559 - 1562 vol.3
Digital Object Identifier 10.1109/ISCAS.1989.100657

AbstractPlus | Full Text: PDF(304 KB)   IEEE CNF
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32. **SONET/SDH pointer processor implementations**
Klein, M.; Urbansky, R.S.;
Global Telecommunications Conference, 1994. GLOBECOM '94. 'Communications: The Global Bri
28 Nov.-2 Dec. 1994 Page(s):655 - 660 vol.1
Digital Object Identifier 10.1109/GLOCOM.1994.513599

AbstractPlus | Full Text: PDF(680 KB)   IEEE CNF
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33. **Power delivery design and analysis on a network processor board**
Cui, W.; Aspnes, B.; Parmar, P.; Suryakumar, M.;
Electromagnetic Compatibility, 2004. EMC 2004. 2004 InternationalSymposium on
Volume 1, 9-13 Aug. 2004 Page(s):231 - 236 vol.1

AbstractPlus | Full Text: PDF(672 KB)   IEEE CNF
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34. **EMI hardening of signal processor subsystem to meet military standard (a case study)**
Sairam, R.M.;
Electromagnetic Interference and Compatibility '99. Proceedings of the International Conference or
6-8 Dec. 1999 Page(s):25 - 28

AbstractPlus | Full Text: PDF(176 KB)   IEEE CNF
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35. **An investigation of PCB radiated emissions from simultaneous switching noise**
Radu, S.; Hockanson, D.;
Electromagnetic Compatibility, 1999 IEEE International Symposium on
Volume 2, 2-6 Aug. 1999 Page(s):893 - 898 vol.2
Digital Object Identifier 10.1109/ISEMC.1999.810174

AbstractPlus | Full Text: PDF(516 KB)   IEEE CNF
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36. **Digital map integration in advanced tactical cockpits**
Farmer, R.G.;
Digital Avionics Systems Conference, 1990. Proceedings., IEEE/AIAA/NASA 9th
15-18 Oct. 1990 Page(s):657 - 662
Digital Object Identifier 10.1109/DASC.1990.111363

AbstractPlus | Full Text: PDF(540 KB)   IEEE CNF
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37. **An equivalent transmission-line model containing dispersion for high-speed digital lines-wit Implementation**
Scarlatti, A.; Holloway, C.L.;
Electromagnetic Compatibility, IEEE Transactions on
Volume 43, Issue 4, Nov. 2001 Page(s):504 - 514
Digital Object Identifier 10.1109/15.974629

AbstractPlus | References | Full Text: PDF(239 KB)   IEEE JNL

38. **The Motorola 68060 microprocessor**
Circello, J.; Goodrich, F.;
Compcon Spring '93. Digest of Papers.
22-26 Feb. 1993 Page(s):73 - 78
Digital Object Identifier 10.1109/CMPCON.1993.289639

AbstractPlus | Full Text: PDF(444 KB) IEEE CNF
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39. **Configuration-based architecture for high speed and general-purpose protocol processing**
Liu, D.; Nordqvist, U.; Svensson, C.;
Signal Processing Systems. 1999. SiPS 99. 1999 IEEE Workshop on
20-22 Oct. 1999 Page(s):540 - 547
Digital Object Identifier 10.1109/SIPS.1999.822360

AbstractPlus | Full Text: PDF(304 KB) IEEE CNF
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40. **Hardware/software co-design language compatible with VHDL**
Wong, S.Y.;
WESCON/98
15-17 Sept. 1998 Page(s):78 - 83
Digital Object Identifier 10.1109/WESCON.1998.716424

AbstractPlus | Full Text: PDF(368 KB) IEEE CNF
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41. **Study on the key-problems for digital controlled multiple parallel active power filter**
Wanjun Lei; Fang Zhuo; Hongyu Li; Zhaoan Wang;
Power Electronics and Motion Control Conference. 2004. IPEMC 2004. The 4th International
Volume 1, 2004 Page(s):254 - 259 Vol.1

AbstractPlus | Full Text: PDF(855 KB) IEEE CNF
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42. **REMIS: the distributed data base solution**
Tuffey, P.J.; Browning, M.; Robison, B.;
Aerospace and Electronics Conference. 1988. NAECON 1988.. Proceedings of the IEEE 1988 Nat
23-27 May 1988 Page(s):1310 - 1317 vol.4
Digital Object Identifier 10.1109/NAECON.1988.195174

AbstractPlus | Full Text: PDF(644 KB) IEEE CNF
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43. **Characterisation of microcontroller susceptibility to radio frequency interference**
Baffreau, S.; Bendhia, S.; Ramdani, M.; Sicard, E.;
Devices. Circuits and Systems. 2002. Proceedings of the Fourth IEEE International Caracas Confe
17-19 April 2002 Page(s):I031-1 - I031-5
Digital Object Identifier 10.1109/ICCDCS.2002.1004088

AbstractPlus | Full Text: PDF(585 KB) IEEE CNF
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44. **A persistent rescheduled-page cache for low overhead object code compatibility in VLIW ar**
Conte, T.M.; Sathaye, S.W.; Banerjia, S.;
Microarchitecture. 1996. MICRO-29. Proceedings of the 29th Annual IEEE/ACM International Sym
2-4 Dec. 1996 Page(s):4 - 13
Digital Object Identifier 10.1109/MICRO.1996.566445

AbstractPlus | Full Text: PDF(1024 KB) IEEE CNF
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45.
Designing a Twinax communication circuit

Zhang Shengbin; Fan Xiaoya; Gao Deyuan; Mou Chengyu;
ASIC, 1996, 2nd International Conference on
21-24 Oct. 1996 Page(s):175 - 178
Digital Object Identifier 10.1109/ICASIC.1996.562780

AbstractPlus | Full Text: PDF(312 KB)   IEEE CNF
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46. **A scalable and fully distributed architecture for Ethernet switching**
Herbert, M.; Primet, P.; Tourancheau, B.; Lefevre, L.;
High Performance Switching and Routing, 2002, Merging Optical and IP Technologies, Workshop
26-29 May 2002 Page(s):234 - 238
Digital Object Identifier 10.1109/HPSR.2002.1024242

AbstractPlus | Full Text: PDF(556 KB)   IEEE CNF
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47. **Data cache architecture of the superscalar by scheduling patterns**
Kato, T.; Ono, T.; Bagherzadeh, N.;
System Sciences, 1993, Proceeding of the Twenty-Sixth Hawaii International Conference on
Volume ii, 5-8 Jan. 1993 Page(s):219 - 220 vol.2
Digital Object Identifier 10.1109/HICSS.1993.284107

AbstractPlus | Full Text: PDF(184 KB)   IEEE CNF
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48. **The first IA-64 microprocessor: a design for highly-parallel execution**
Singer, G.; Rusu, S.;
Solid-State Circuits Conference, 2000, Digest of Technical Papers, ISSCC, 2000 IEEE Internationa
7-9 Feb. 2000 Page(s):422 - 423
Digital Object Identifier 10.1109/ISSCC.2000.839843

AbstractPlus | Full Text: PDF(218 KB)   IEEE CNF
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49. **Improving binary compatibility in VLIW machines through compiler assisted dynamic resch**
Biglari-Abhari, M.; Eshraghian, K.; Liebelt, M.J.;
Euromicro Conference, 2000, Proceedings of the 26th
Volume 1, 5-7 Sept. 2000 Page(s):386 - 393 vol.1
Digital Object Identifier 10.1109/EURMIC.2000.874657

AbstractPlus | Full Text: PDF(752 KB)   IEEE CNF
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50. **Dynamic reconfiguration with binary translation: breaking the ILP barrier with software com**
Beck, A.C.S.; Carro, L.;
Design Automation Conference, 2005, Proceedings, 42nd
13-17 June 2005 Page(s):732 - 737

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<u>System-on-Chip. 2004. Proceedings. 2004 International Symposium on</u>
16-18 Nov. 2004 Page(s):85 - 88
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<u>Position Location and Navigation Symposium. 1990. Record. 'The 1990's - A Decade of Excellence</u>
<u>Sciences'. IEEE PLANS '90., IEEE</u>
20-23 March 1990 Page(s):464 - 469
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<u>Heterogeneous Computing Workshop. 2000. (HCW 2000) Proceedings. 9th</u>
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Lempel, O.; Peleg, A.; Weiser, U.;
Compcon '97, Proceedings, IEEE
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Digital Object Identifier 10.1109/CMPCON.1997.584723

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Bonnet, P.; Vernet, R.; Paladian, F.;
Electromagnetic Compatibility, 2003. EMC '03. 2003 IEEE International Symposium on
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Digital Object Identifier 10.1109/ICSMC2.2003.1429100

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Johnson, L.; Colvin, N.; Vandette, B.; Heitman, P.; Keller, S.; Dice, D.; Goodell, R.; Nahabedian, M
COMPCON Spring '89, Thirty-Fourth IEEE Computer Society International Conference; Intellectual
Papers.
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   AbstractPlus | References | Full Text: PDF(286 KB)   IEEE JNL
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   Sukhee Cho; Kugjin Yun; Namho Hur; Soo-In Lee;
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    Circuits and Systems, 1989., IEEE International Symposium on
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    Caulk, B.; Desai, S.; Gavrielov, M.; Harper, G.; Jones, D.; Kwong, M.; Murzello, M.; Oke, T.; Patel,

Yang, R.;
Computer Design: VLSI in Computers and Processors, 1991. ICCD '91. Proceedings., 1991 IEEE I
Conference on
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Klein, M.; Urbansky, R.S.;
Global Telecommunications Conference, 1994. GLOBECOM '94. 'Communications: The Global Bri
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Cui, W.; Aspnes, B.; Parmar, P.; Suryakumar, M.;
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Digital Avionics Systems Conference, 1990. Proceedings., IEEE/AIAA/NASA 9th
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System Sciences, 1993. Proceeding of the Twenty-Sixth Hawaii International Conference on
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